

# Department of EE DEI Agra Hosted Seminar on VLSI & Semiconductor Industry Career Roadmap in India

Prof. C. Patvardhan (Director DEI) was the chief patron, and Dr. Gufran Ahmad was the organizer of the seminar.

Organizing team: Aarat prasad, Kartikay Bharadwaj and Priya Rajpuri.



Participants at the seminar organised by EE at FAB LAB, on Friday, Jan-31<sup>st</sup> 2025.

The Department of Electrical Engineering (EE), Faculty of Engineering at the Dayalbagh Educational Institute of Technology (DEI) Agra, hosted a technical seminar titled "VLSI & Semiconductor Industry Career Roadmap." The seminar was presented by Dr. Santunu Sarangi, associated with Sevy Multimedia Technologies Pvt. Ltd. as an Analog IC Design consultant and VLSI Trainer. He also worked for Micron Technology through Sevy Multimedia. The event, held at the FAB LAB, aimed to inspire participants with insights into emerging semiconductor & VLSI industries in India. Dr. Santunu captivated the audience with his profound expertise in VLSI Design practices showcasing numerous examples. He provided insights into job opportunities in the area of Analog and Digital VLSI design as well as in the PDK development. Dr. Santunu stressed the role of open-source CAD tools in developing VLSI Design skills. Participants engaged in interactive discussions and a Q&A session. Dr. Santunu highlighted the importance of strong fundamentals, adaptability, and networking in today's dynamic industrial environment. The seminar left a lasting impact on its participants, equipping them with valuable insights and guidance for the emerging VLSI industry workforce.